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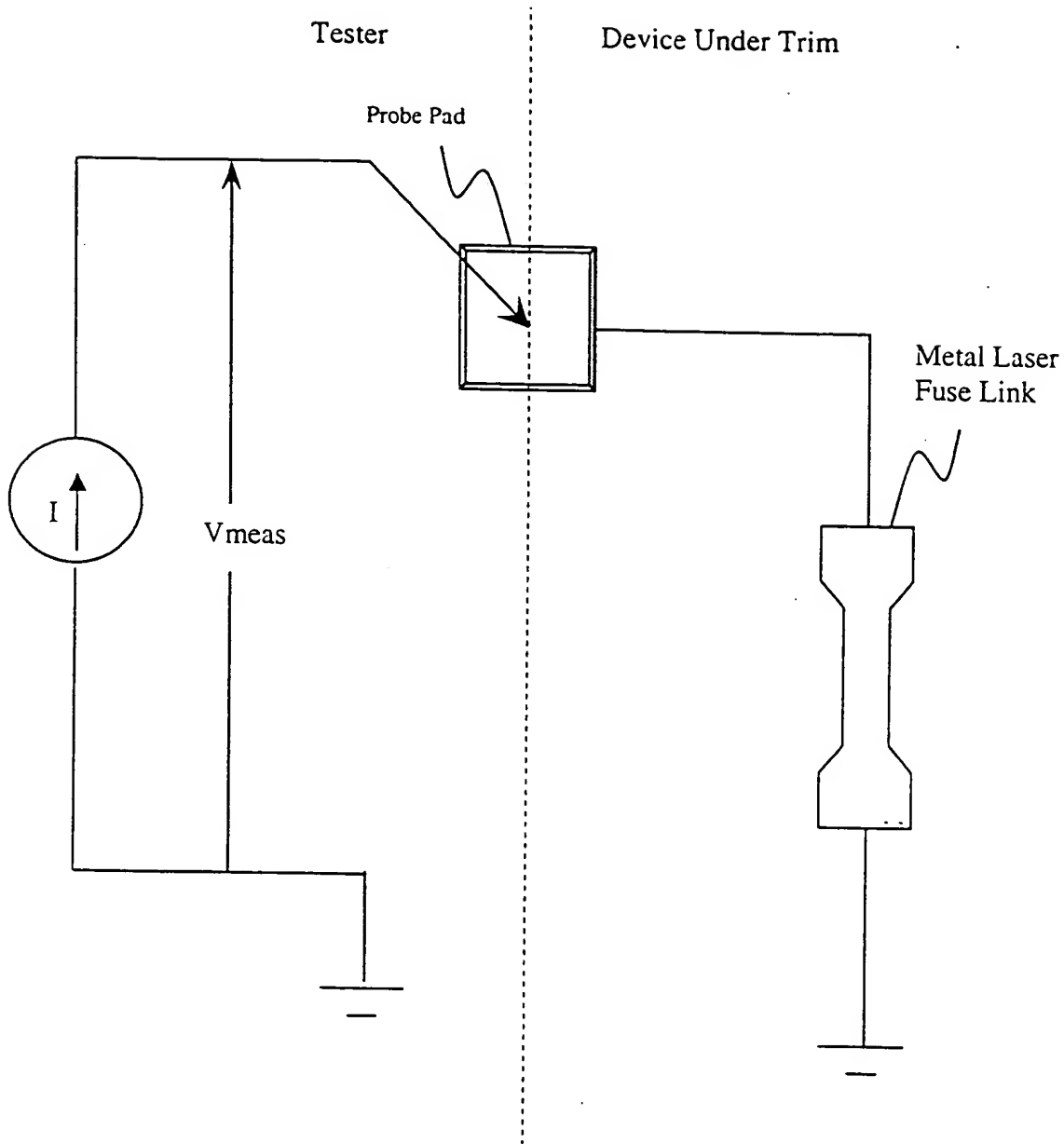


Figure 1
(Prior Art)

TI 37201

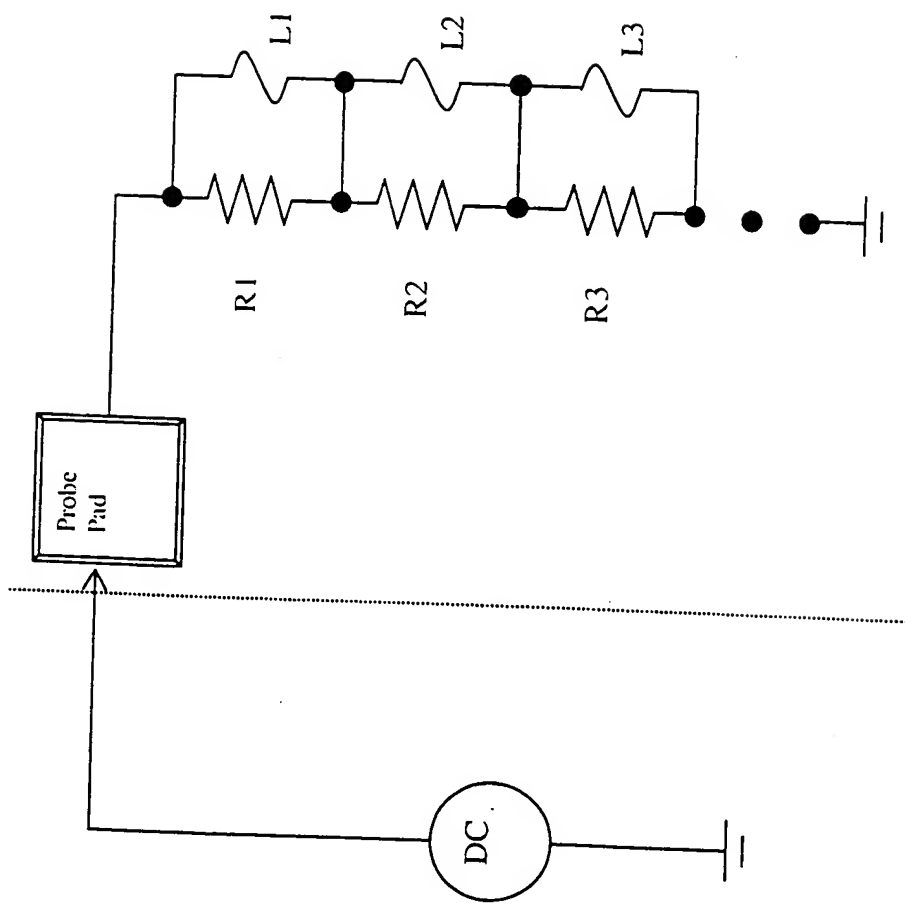


Figure 2

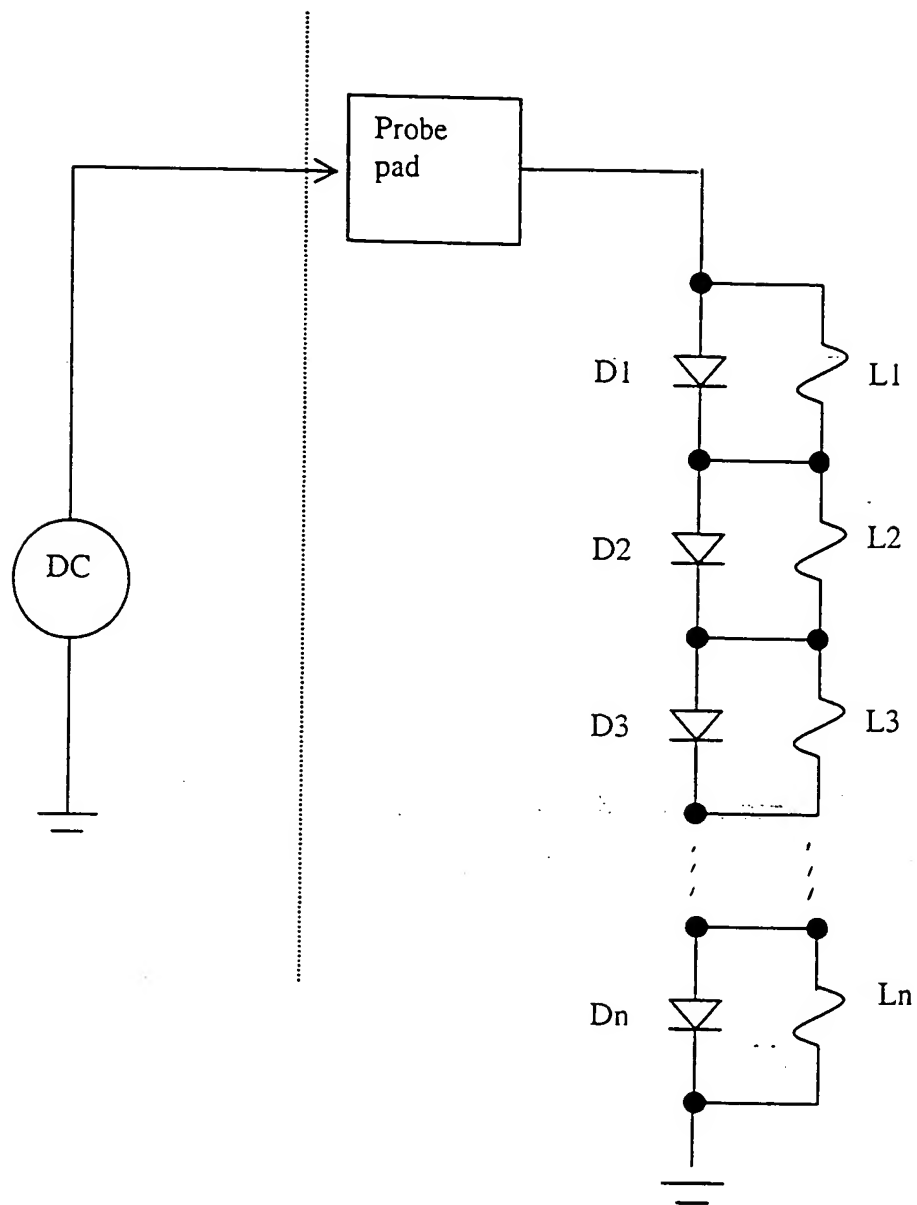


Figure 3

TI 37201

Maximum Number of Component/Link Trim Record Pairs per Network

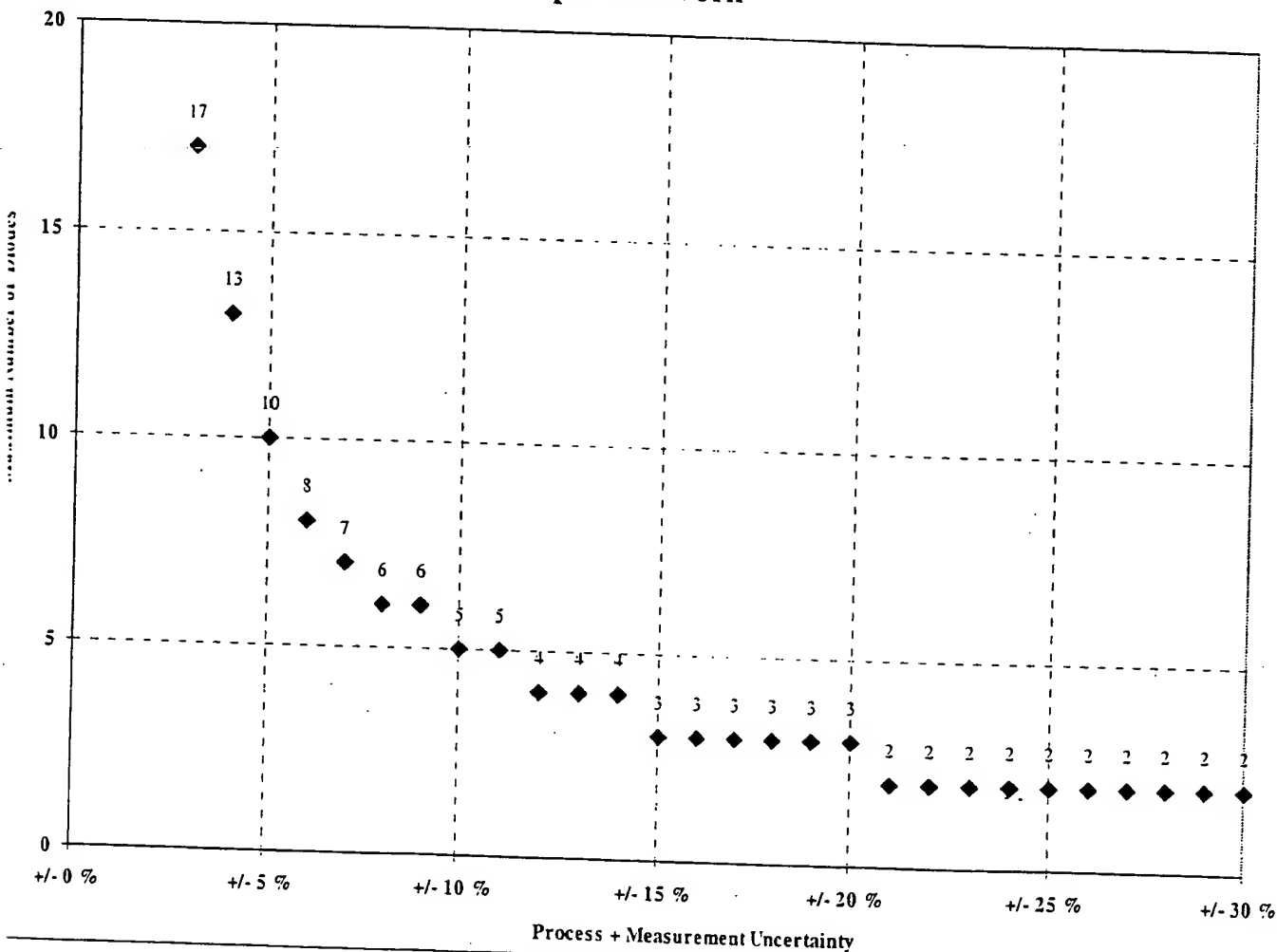


Figure 4

Test Equipment

Circuit structure

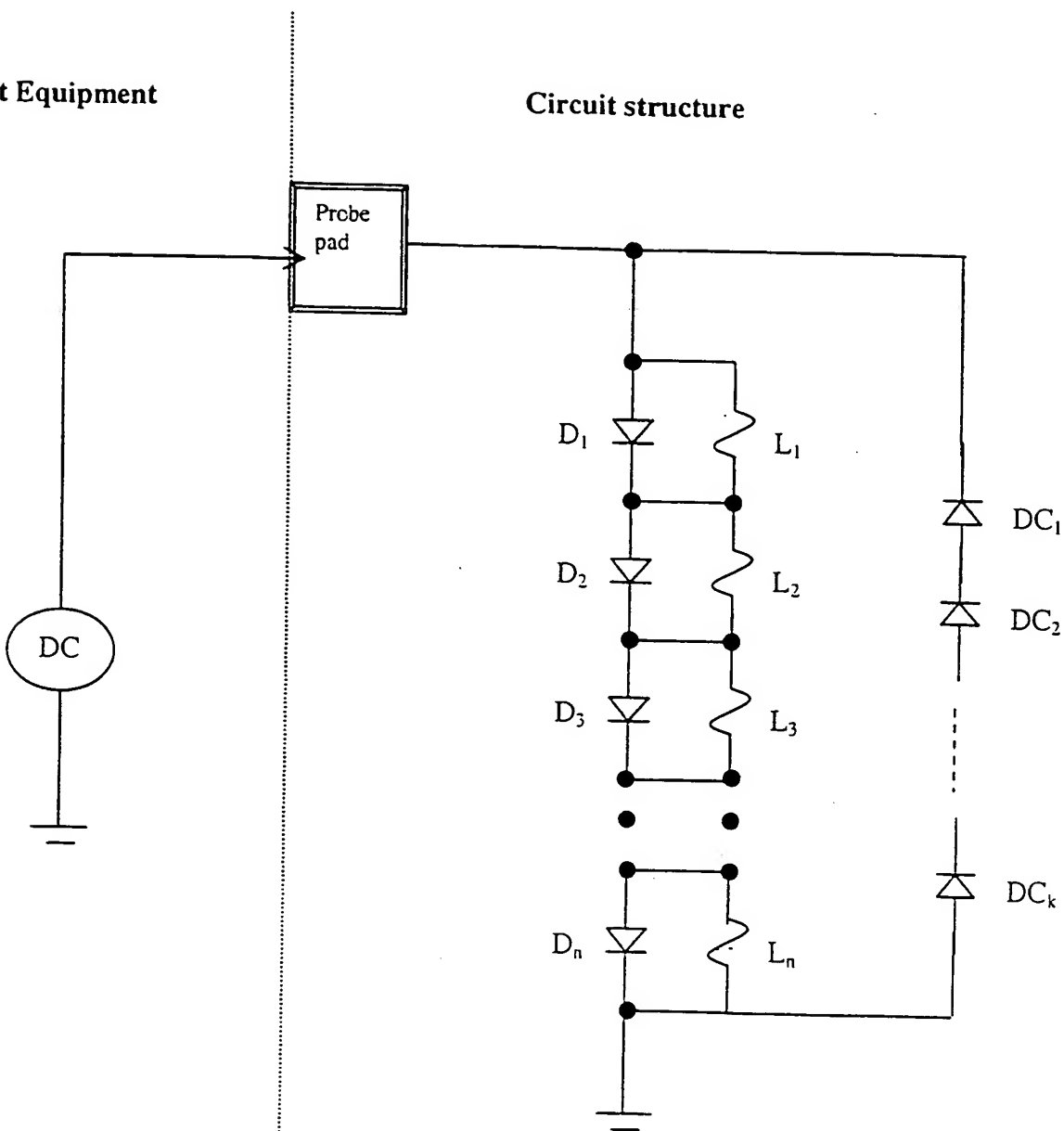


Figure 5

TI 37201

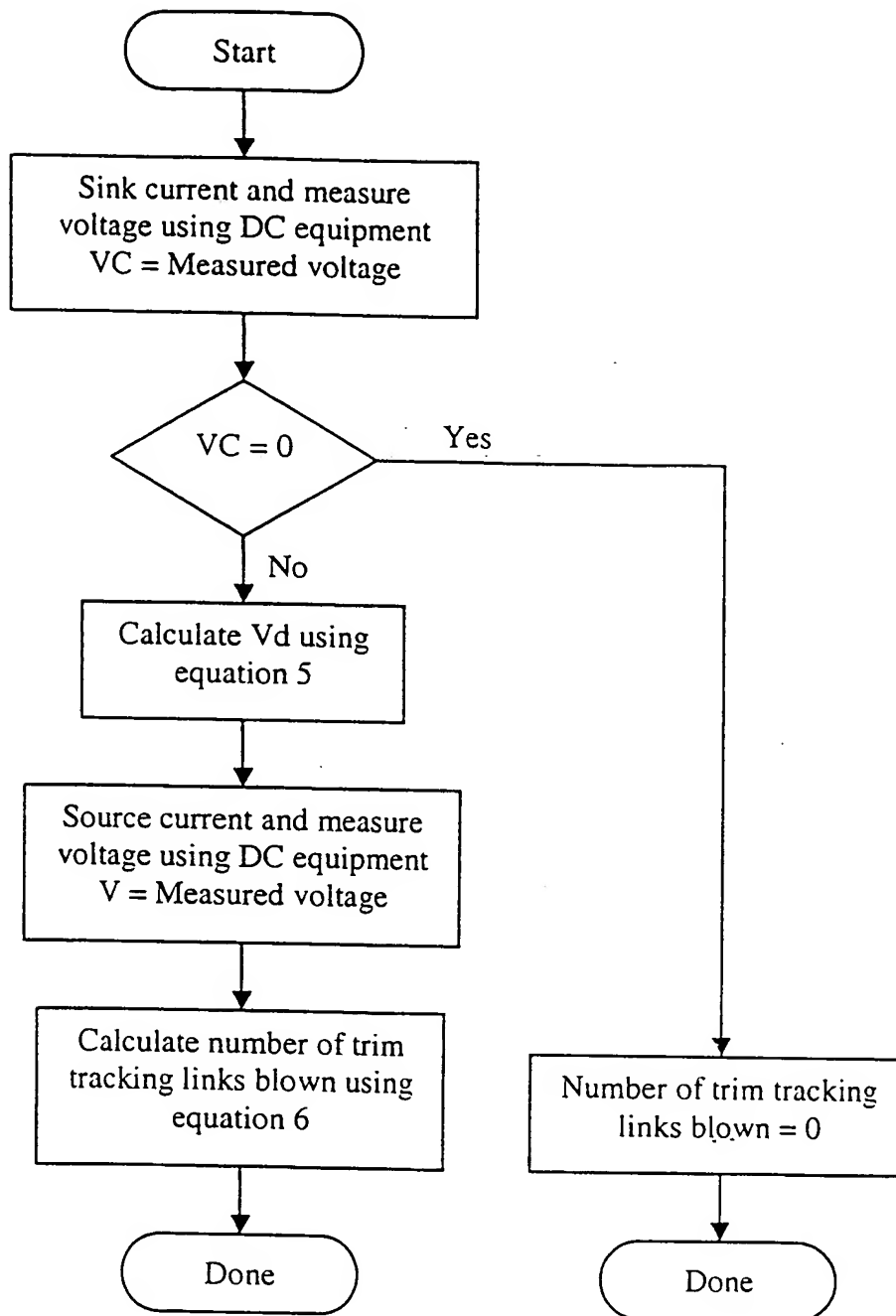


Figure 6

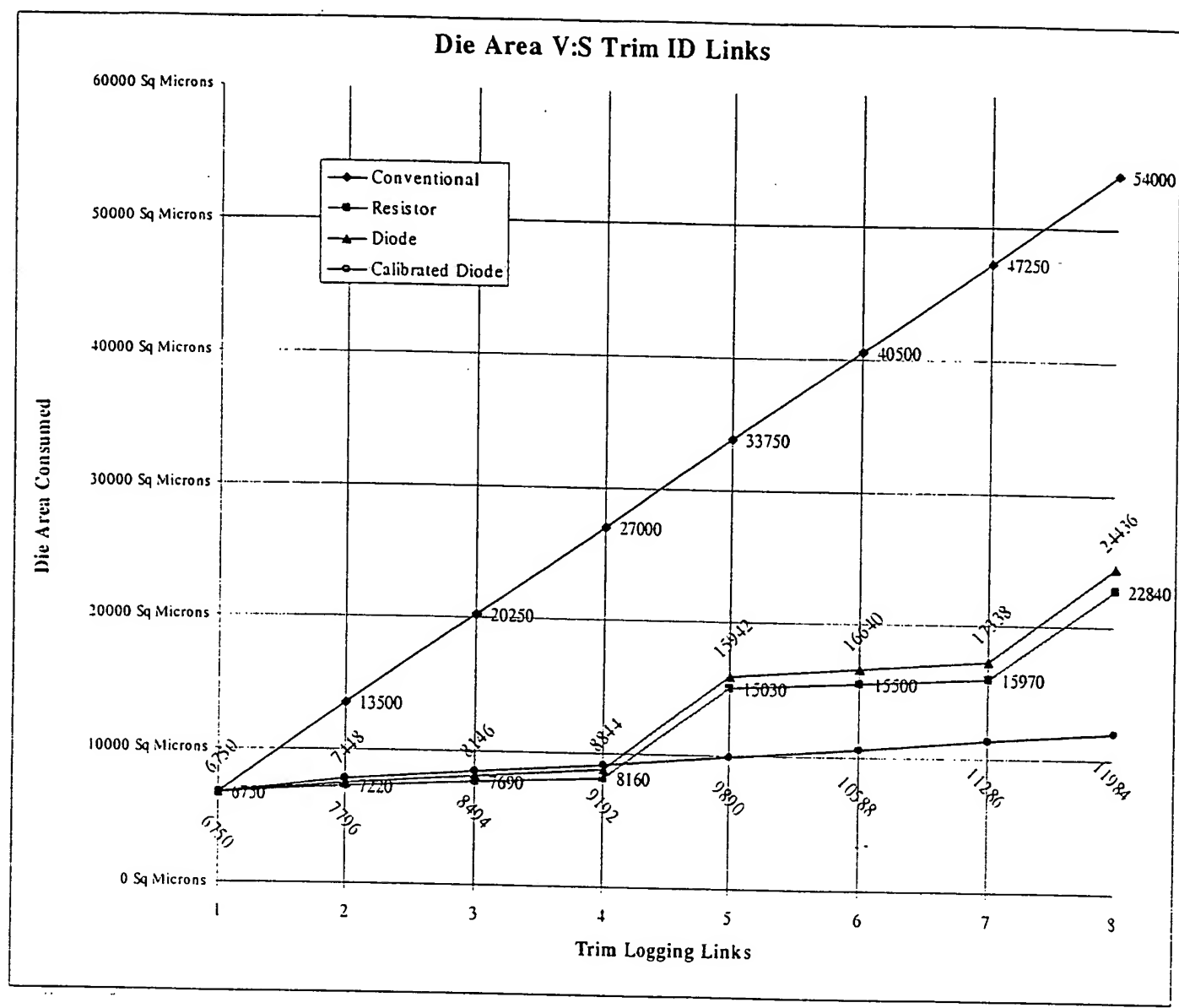


Figure 7